

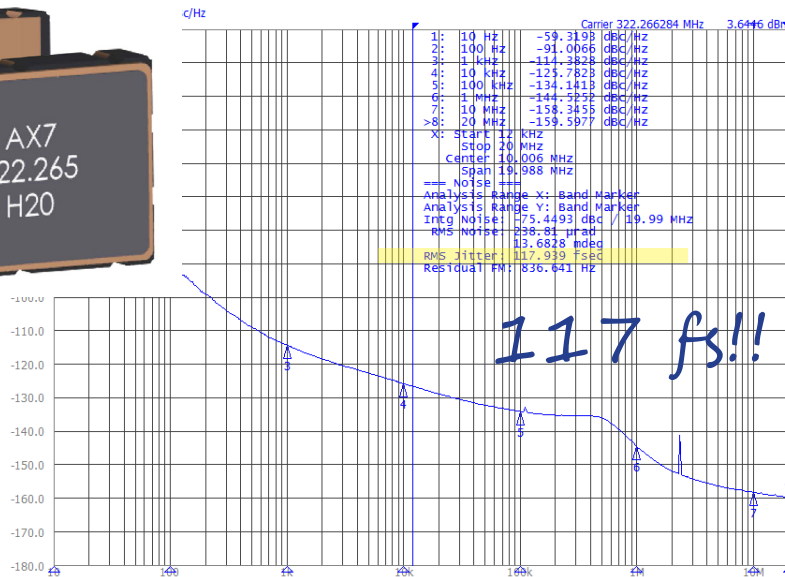
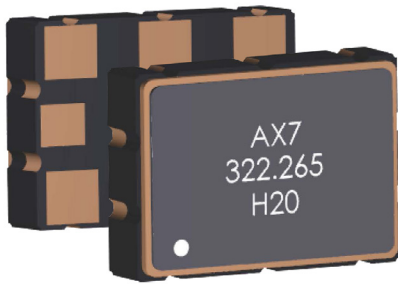
AX7 ClearClock™

Power Optimized 0.12ps for RF and Networking

1 week samples

The AX7 ClearClock™ XO for next generation networking and RF applications leads the industry in low power consumption while maintaining typical jitter of 0.12ps ideal for clocking serial data links beyond 56Gbps. The series features the widest factory configurable frequency range from 50MHz to 2.1GHz for this performance class. Other factory configurable options include supply voltage of 3.3V, 2.5V, or 1.8V, -20°C to +70°C or -40°C to +85°C operating temperature range with either ±50ppm or ±100ppm all inclusive frequency stability over 10 year product life. The AX7 series is optimized for superior rms jitter performance (0.12ps typ), while consuming 129mW in industry standard 5x7mm package.

ClearClock™ 322.265625MHz | HSCL | 2.5V



120fs TYP JITTER
(150fs MAX f>200MHz)

WIDEST FREQUENCY IN ITS CLASS
FROM 50 TO 2100MHz

LOWEST IN-CLASS POWER
CONSUMPTION 70mA MAX I_{DD} (LVDS)

For more information see complete AX7 Datasheet

APPLICATIONS

RF Systems

High speed SERDES up to 56Gbps
High Performance Computing (HPC)
Network Processors

PCI Express

BTS/ Basestations
Test and measurement
FPGA/Processors

100/400Gb Ethernet

Server, Storage, Fibre Channel
Networking and communications
Backplane/Chip to Chip Buses