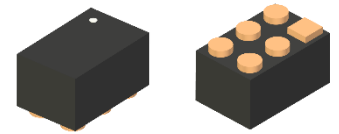


### Description

The ASWD-S2-0001 is a low loss, high isolation, SOI SPDT Radio-Frequency switch. Outstanding performance for ultra-low tuning-on resistance and ultra-high linearity are making its perfectly to use for antenna tuning application for no matter high band or low band switch in GSM/WCDMA/LTE handset application. The ASWD-S2-0001 is packaged in a RoHS-compliant, compact DFN 1.1mm x 0.7mm x 0.55mm surface-mount leadless package



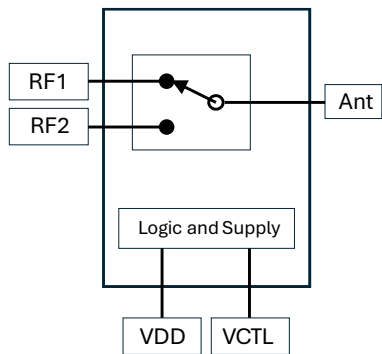
### Key Features

- Frequency Range: 0.1 to 8.0GHz
- Low Insertion Loss: 0.37dB at 2.7GHz
- High Isolation: 25dB at 2.7GHz
- GPIO Control for SPDT
- DFN-6L Package: 1.1mm x 0.7mm

### Typical Applications

- Smart Phones, Tablets, PCs
- GSM/WCDMA/LTE band and Mode Switching
- Antenna Selection / Tuning Switch
- RF Combo Modules (Wi-Fi/BT, LTE/GNSS)
- AR/VR, Wi-Fi Speakers
- Access Point, Routers and Gateways

### Functional Block Diagram



### Ordering Information

Part No.	Description
ASWD-S2-0001	0.1~ 8GHz SPDT Antenna Switch on Cut Tape
ASWD-S2-0001-T	0.1~ 8GHz SPDT Antenna Switch on Tape & Reel
ASWD-S2-0001-EVB	0.1~ 8GHz SPDT Antenna Switch EVB

### Absolute Maximum Ratings

Parameter	Symbol	Absolute Maximum			Unit
Supply Voltage	V <sub>DD</sub>	1.6	-	3.6	V
Control Voltage	V <sub>CTL</sub>			V <sub>DD</sub>	V
Max Input Power	P <sub>INMAX</sub>	-	-	+35	dBm
Operating Temperature	T <sub>OP</sub>	-40	-	85	°C
Storage Temperature	T <sub>STG</sub>	-55	-	150	°C
Electrostatic Discharge, HBM <sup>1</sup>	V <sub>ESD</sub>	-	-	±1000	V
Electrostatic Discharge, CDM <sup>2</sup>		-	-	±1000	V

1. HBM: ESDA/JEDEC JS-001-2017

2. CDM: ESDA/JEDEC JS-002-2018

Operation of this device outside the parameter ranges given above may cause permanent damage.

### Recommended Operating Conditions

Parameter	Min.	Typ.	Max.	Unit
V <sub>DD</sub>	1.6	2.8	3.1	V
P <sub>in</sub> (RFC – RFX), CW, 50 Ω			34.5	dBm
T <sub>j</sub> at MTTF>105 hrs.	-	150	-	°C

Electrical specifications are measured at specified test conditions. Specifications are not guaranteed over all recommended operating conditions.

### RF Electrical Specifications

Parameters		Condition	Values			Unit
			Minimum	Typical	Maximum	
Insertion Loss	IL	0.1-1.0 GHz	-	0.35	0.50	dB
		1.0-2.2 GHz	-	0.35	0.55	
		2.2-2.7 GHz	-	0.37	0.60	
		2.7-4.0 GHz		0.57	0.75	
		4.0-8.0 GHz		0.57	0.75	
Isolation	ISL	0.1-1.0 GHz	30	35	-	dB
		1.0-2.2 GHz	28	33	-	
		2.2-2.7 GHz	20	25	-	
		2.7-4.0 GHz	15	18		
		4.0-8.0 GHz	12	14		
Return Loss	RL	0.1-2.7 GHz	-	13.9	20.8	dB
Input P <sub>0.1dB</sub>	P <sub>0.1dB</sub>	0.8-2.7 GHz	-	34.5	-	dBm
2 <sup>nd</sup> Harmonics	2f <sub>0</sub>	P <sub>in</sub> = 26dBm	-	-80	-	dBc
3 <sup>rd</sup> Harmonics	3f <sub>0</sub>	P <sub>in</sub> = 26dBm	-	-75	-	dBc
Turn-on switching time (50% of final VC to 10%/90% of final RF power)	t <sub>sw</sub>	-	-	1.0	-	us

## DC Electrical Specifications

Parameter	Symbol	Absolute Maximum			Unit
Supply Voltage	$V_{DD}$	1.6	2.8	3.1	V
Supply Current	$I_{DD}$	-	10	50	$\mu$ A
Control Voltage	$V_{CTL\_H}$	1.2	1.8	2.85	V
	$V_{CTL\_L}$	-	-	0.4	V
Control Current	$I_{CTL}$	-	2.5	5	$\mu$ A

## Control Logic

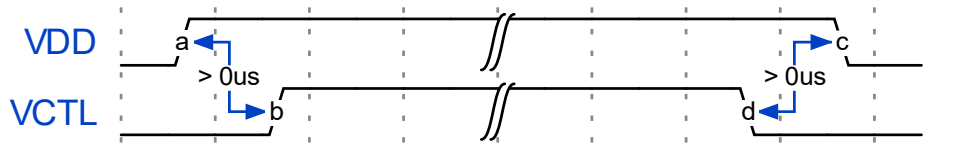
Control Pin	RF1	RF2
0	ON	OFF
1	OFF	ON

### Timing Requirements

It is important that the user adheres to the correct timing sequences in order to avoid leakage power consumption.

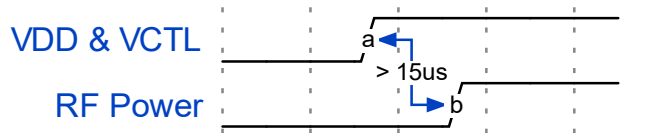
- VDD and VCTL cannot be powered on and off independently from one another. During power-on sequence, the user must power up VDD first, then power up VCTL. During power-off sequence, the user must power off VCTL first, then power off VDD.

In the state of VDD=OFF (0 V) and VCTL=ON (1.8V), it may cause leakage power consumption as ESD protection circuit inside the switch.



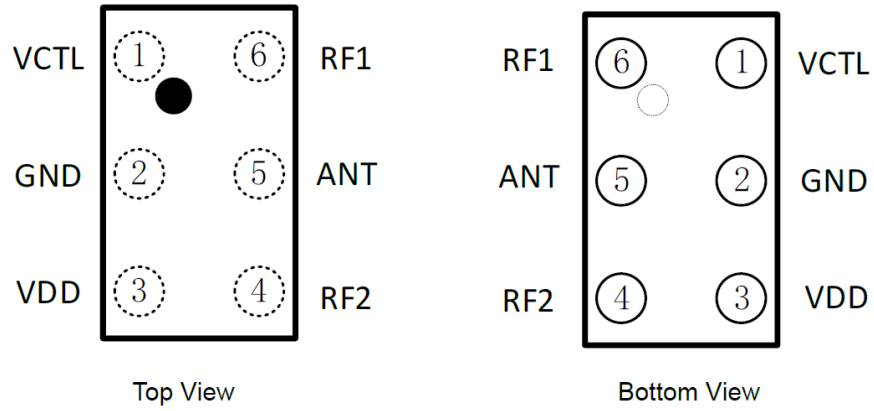
**a: VDD ON    b: VCTL ON    c: VDD OFF    d: VCTL OFF**

- VDD and VCTL must be on for a minimum of 15 us before applying RF power



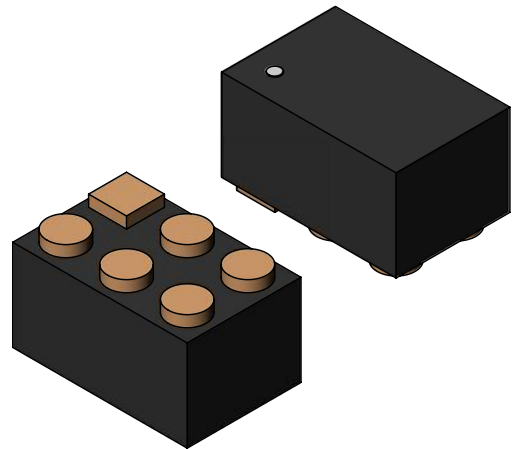
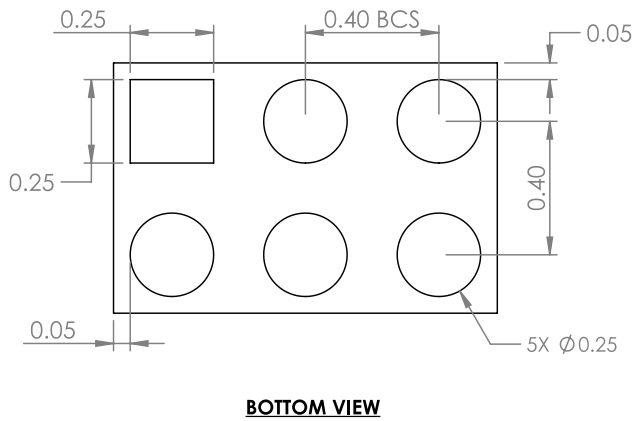
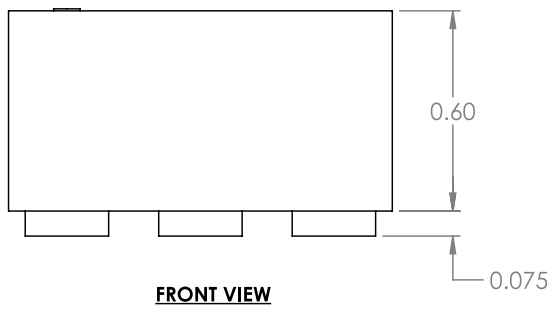
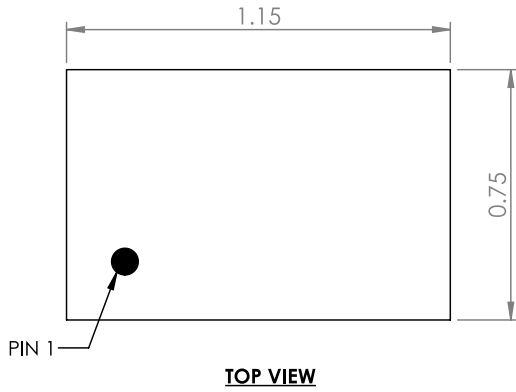
**a: VDD and VCTL ON            b: RF Power Up**

Pin Configuration



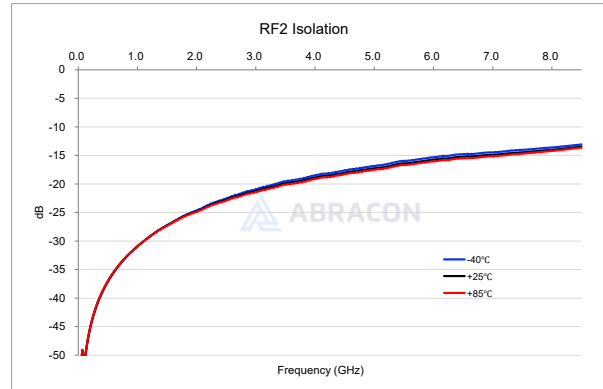
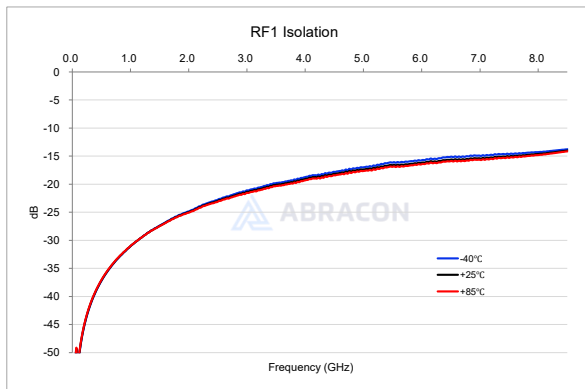
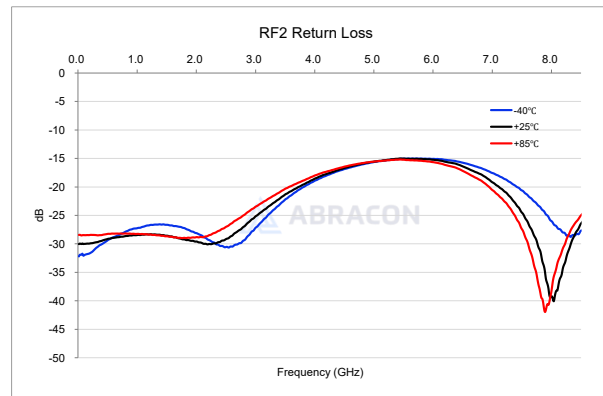
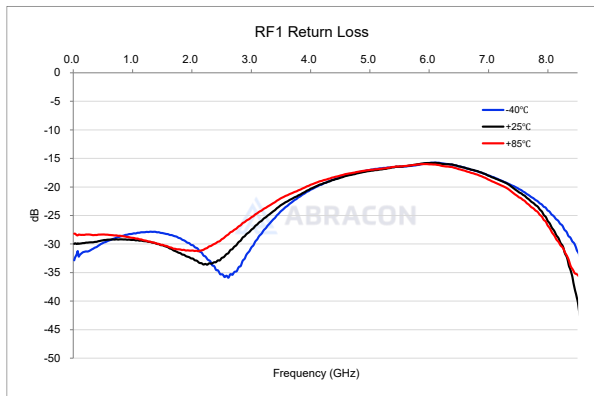
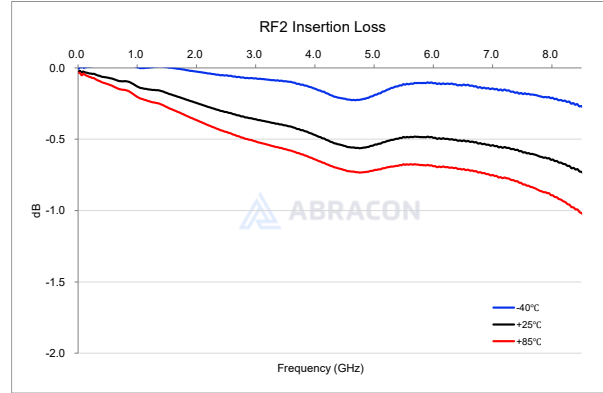
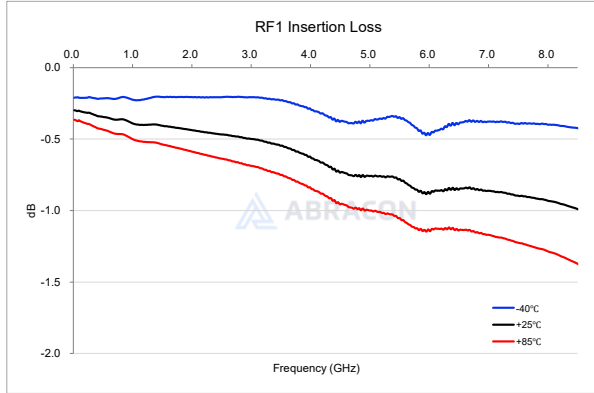
Pin	Name	Description
1	VCTL	Switch Control
2	GND	Ground
3	VDD	DC Control Voltage
4	RF2	RF2 Port
5	ANT	Antenna Port
6	RF1	RF1 Port

**Product Dimensions**



Unit: mm

Performance Plots

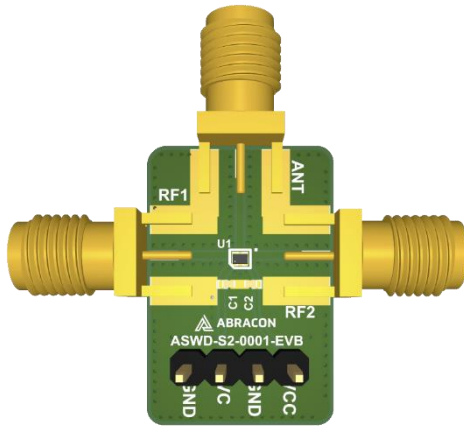


# ASWD-S2-0001

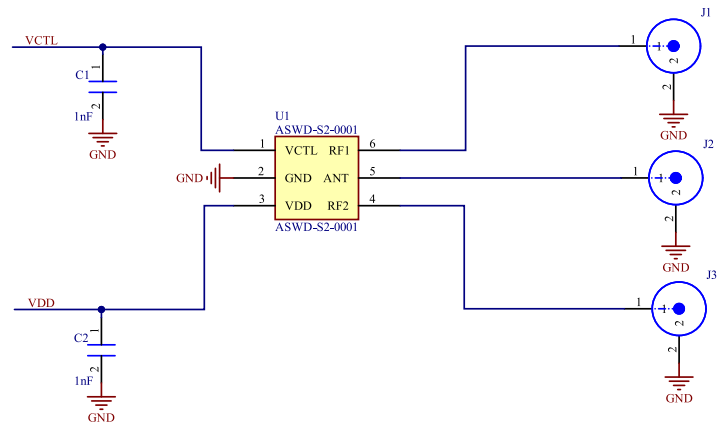
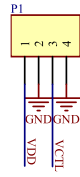
0.1~ 8GHz SPDT Antenna Switch



## Evaluation Board ASWD-S2-0001-EVB



EVB



Schematic

## Bill of Material

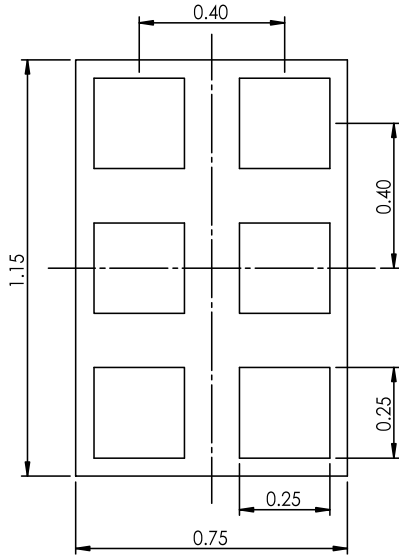
Component	Description	Manufacturer	Manufacturer Part	QTY
U1	SPDT Antenna Switch	Abracorn	ASWD-S2-0001	1
C1, C2	Capacitor (0402)	Murata	GRM022R60J102KE19	2

### NOTES:

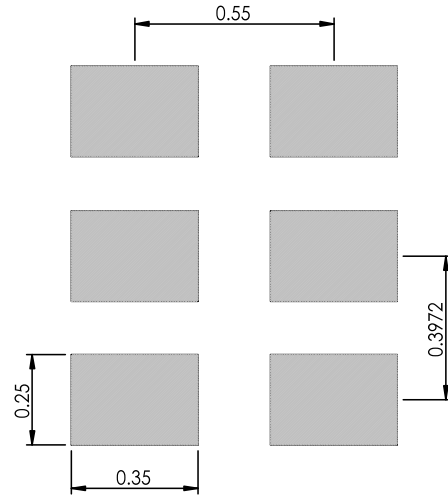
1. Input and output are 50-ohm lines.



IC Footprint & PCB Land Pattern



Footprint



Land Pattern

Reflow Profile [JEDEC J-STD-020]

Solder paste: Sn/3.0Ag/0.5Cu

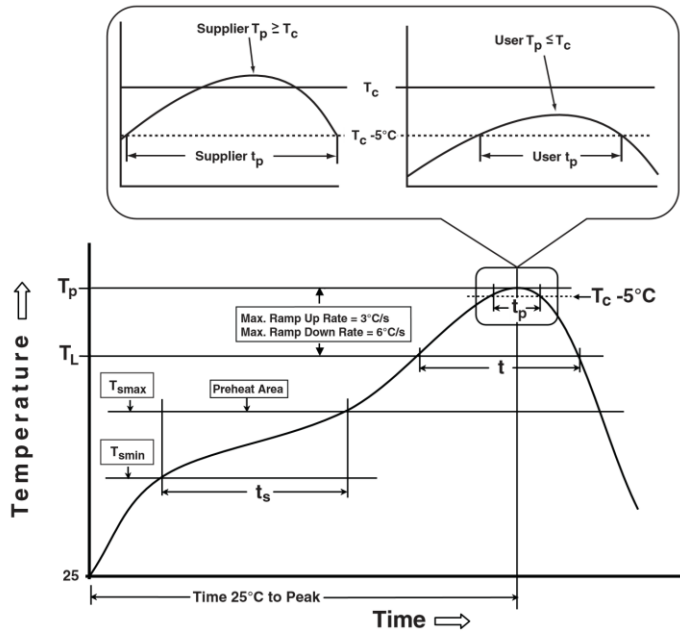


Table 1

SnPb Eutectic Process Classification Temperatures (T <sub>c</sub> )		
Package Thickness	Volume mm <sup>3</sup> <350	Volume mm <sup>3</sup> ≥350
<2.5mm	235°C	220°C
≥2.5mm	220°C	220°C

Table 2

Pb-Free Process Classification Temperatures (T <sub>c</sub> )			
Package Thickness	Volume mm <sup>3</sup> <350	Volume mm <sup>3</sup> 350-2000	Volume mm <sup>3</sup> >2000
<1.6mm	260°C	260°C	260°C
1.6mm - 2.5mm	260°C	250°C	245°C
>2.5mm	250°C	245°C	245°C

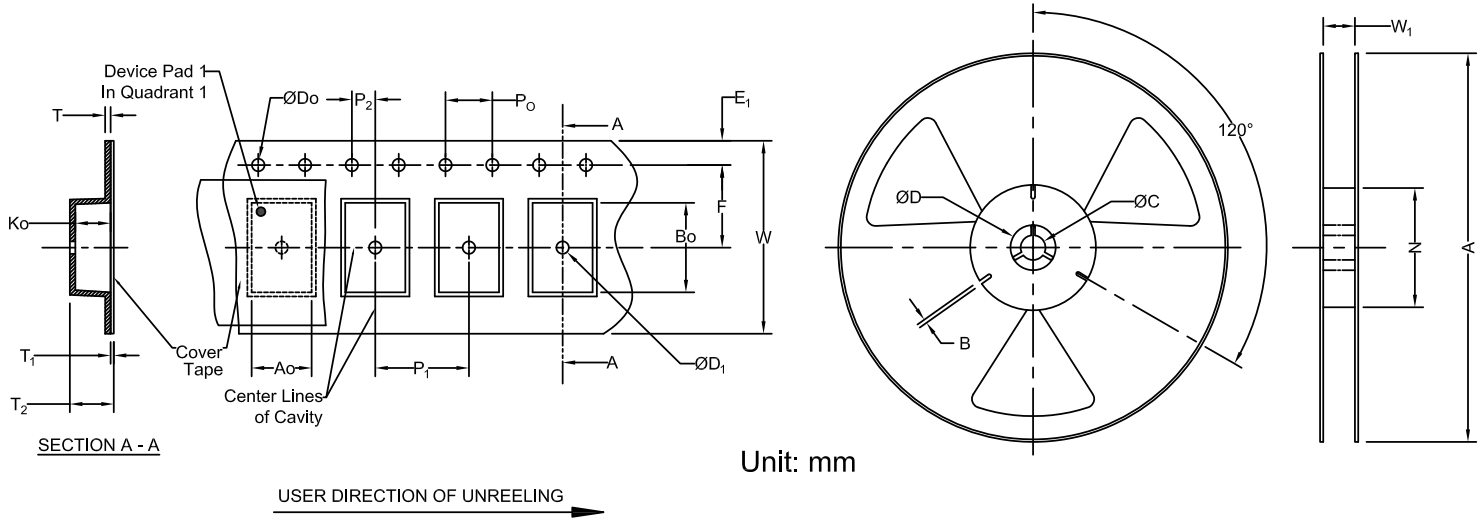
Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Preheat / soak		
Temperature minimum (T <sub>sm</sub> )	100°C	150°C
Temperature maximum (T <sub>smx</sub> )	150°C	200°C
Time (T <sub>sm</sub> to T <sub>smx</sub> ) (t <sub>s</sub> )	60 – 120 sec.	60 – 120 sec.
Average ramp-up rate (T <sub>sm</sub> to T <sub>p</sub> )	3°C/sec. max	3°C/sec. max
Liquidous temperature (T <sub>L</sub> )	183°C	217°C
Time at Liquidous (T <sub>L</sub> )	60 – 90 sec.	60 – 90 sec.
Peak package body temperature (T <sub>p</sub> )*	<b>See Table 1</b>	<b>See Table 2</b>
Time (T <sub>p</sub> )** within 5°C of the specified classification temperature (T <sub>c</sub> )	20 sec.	10 sec.
Ramp-down rate (T <sub>p</sub> to T <sub>smx</sub> )	3°C/sec. max	3°C/sec. max
Time 25°C to peak temperature	6 min. max	8 min. max
Reflow cycles	2 max	2 max

\*Tolerance for peak profile temperature (T<sub>p</sub>) is defined as a supplier minimum and a user maximum.

\*\*Tolerance for time at peak profile temperature (t<sub>p</sub>) is defined as a supplier minimum and a user maximum.

**Packaging**

Tape & Reel Dimension



Carrier Tape Specifications (mm)										
E1	D0	P0	P2	F	P1	W	A0	B0	K0	Reel Qty
1.75 ± 0.1	1.55 ± 0.05	4.0 ± 0.1	2.0 ± 0.05	3.5 ± 0.05	2.0 ± 0.05	8.0 + 0.2/-0.1	0.83 ± 0.05	1.25 ± 0.05	0.55 ± 0.05	5,000

Reel Specifications (mm)					
A	W1	N	B	C	D
177.8	8.4 ± 1.5	53.6 ± 2.0	1.5	13.2 ± 0.3	20.2