ASATDV







2.0 x 1.6 x 0.8 mm **RoHS/RoHS II Compliant** MSL Level = 1



Features

- Continuous Vdd operation from 1.68 $V \sim 3.63 V$
- Low current consumption
- Standby (Power Down) function
- Output waveform CMOS
- Hermetically seam-sealed ceramic package

Applications

- Portable & wearable electronics
- Internet of Things (IoT)
- Consumer electronics
- Industrial control & automation
- Mobile communication

Electrical Specifications

Parameters		Min.	Тур.	Max.	Units	Notes
Frequency Range		9.6		60	MHz	
Operating Temperature Range		-40		+125	°C	See options
Storage Temperature Range		-55		+125	°C	
Overall Frequency Stability [Note 1]		-10		+10	ppm	See options
Supply Voltage (Vdd)		1.68		3.63	V	
Output Load				15	рF	CMOS
Output Voltage	Voh	0.9*Vdd			V	
Output Voltage	V_{OL}			0.1*Vdd		
Aging 1 year @25°C± 3°C	Aging 1 year @25°C±3°C			+1.0	ppm	
Symmetry @ ½ Vdd	Symmetry @ ½ Vdd		50	55	%	
Start-up Time				10.0	ms	
Rise and Fall Time (Tr/Tf) @10%Vdd-90%Vdd, 15pF load	Vdd = 1.8V to 3.63V			5.0		
	Vdd = 1.68V to 1.79V			8.0	ns	
RMS Phase Jitter @25°C± 3°C (10 - 39MHz: 12kHz to 5MHz) (>39MHz: 12kHz to 20MHz)				1.0	ps	
Tri-state function		"1" (VIH≥0.7*Vdd) or Open: Oscillation; "0" (VIL<0.3*Vdd): No Oscillation/Hi Z			V	
Disable Current				10.0	μA	

Note 1:

Overall frequency stability includes initial frequency tolerance @25°C± 3°C and stability over the operating temperature range.



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ESD Sensitive (Pb)



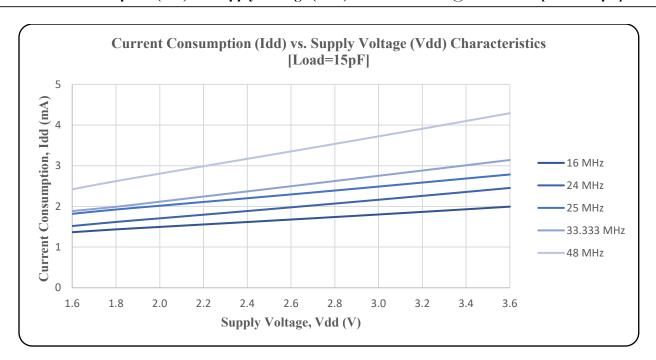


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Electrical Specifications *continued*

Parameters	Min.	Тур.	Max.	Units	Notes
Supply Current (Idd) into 15pF Load @25°C± 3°C @ Vdd=3.3V			4.8		9.60MHz to 20.00MHz
			5.5		20.01MHz to 30.00MHz
			6.0	mA	30.01MHz to 40.00MHz
			7.0		40.01MHz to 50.00MHz
			8.0		50.01MHz to 60.00MHz
			4.3		9.60MHz to 20.00MHz
Supply Current (Idd) into 15nE Load @25°C+ 2°C			5.0	mA	20.01MHz to 30.00MHz
Supply Current (Idd) into 15pF Load @25°C± 3°C @ Vdd=2.5V			5.5		30.01MHz to 40.00MHz
			6.5		40.01MHz to 50.00MHz
			7.5		50.01MHz to 60.00MHz
			3.8	mA	9.60MHz to 20.00MHz
Supply Current (Idd) into 15pF Load @25°C± 3°C			4.5		20.01MHz to 30.00MHz
			5.0		30.01MHz to 40.00MHz
@ Vdd=1.8V			6.0		40.01MHz to 50.00MHz
			7.0		50.01MHz to 60.00MHz

Typical Current Consumption (Idd) vs. Supply Voltage (Vdd) Characteristics @25C°C±3°C [Load=15pF]

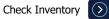




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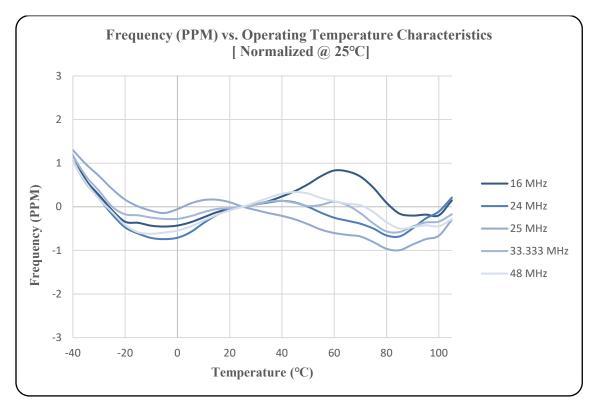
ESD Sensitive (Pb)



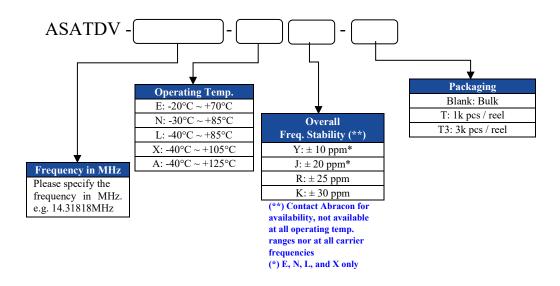


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Typical Frequency vs. Operating Temperature Characteristics @ Vdd = 2.5V [Load=15 pF]



Part Identification





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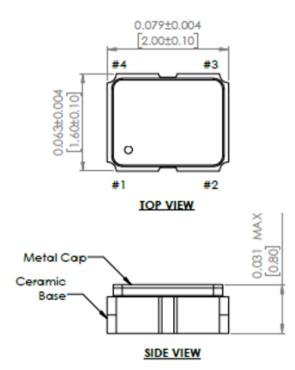
ESD Sensitive (Pb)

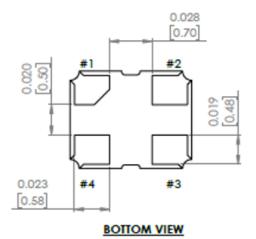




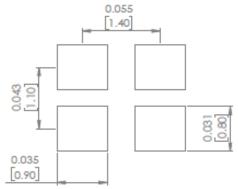
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Mechanical Dimensions





Recommended Land Pattern



Pin #	Function
1	Tri-State
2	GND
3	Output
4	Vdd

Note: Recommended to use approximately 0.01µF bypass capacitor between PIN 2 and PIN 4

INH Function				
PAD #1	PAD #3 (Output)			
Open	Active			
"H" Level	Active			
"L" Level	High Z (No Oscillation)			

Dimensions: inches (mm)



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ESD Sensitive Po

Check Inventory (>)



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Reflow Profile [JEDEC J-STD-020]

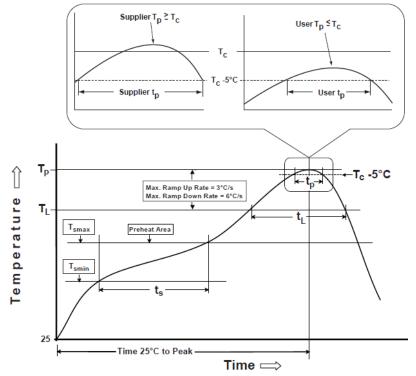


Table 1 **SnPb Eutectic Process** Classification Temperatures (T_c) Package Volume mm³ Volume mm³ Thickness <350 <u>></u>350 235 °C 220 °C <2.5 mm ≥2.5 mm 220 °C 220 °C

Table 2						
Pb-Free Process						
Classification	Classification Temperatures (Tc)					
Package Thickness	Volume mm³ <350	Volume mm ³ 350-2000	Volume mm³ >2000			
<1.6 mm	260 °C	260 °C	260 °C			
1.6 mm - 2.5 mm	260 °C	250 °C	245 °C			
>2.5 mm	250 °C	245 °C	245 °C			

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Preheat / soak		
Temperature minimum (T _{smin})	100°C	150°C
Temperature maximum (T _{smax})	150°C	200°C
Time (T _{smin} to T _{smax}) (t _s)	60 - 120 sec.	60 - 120 sec.
Average ramp-up rate (T _{smax} to T _P)	3°C/sec. max	3°C/sec. max
Liquidous temperature (T _L)	183°C	217°C
Time at liquidous (t _L)	60 - 150 sec.	60 - 150 sec.
Peak package body temperature (T _P)*	see Table 1	see Table 2
Time $(t_p)^{**}$ within 5°C of the specified classification temperature (T_c)	20 sec.	30 sec.
Ramp-down rate (T _p to T _{smax})	6°C/sec. max	6°C/sec. max
Time 25°C to peak temperature	6 min. max	8 min. max
Reflow cycles	2 max	2 max

^{*}Tolerance for peak profile temperature (T_P) is defined as a supplier minimum and a user maximum.



^{**}Tolerance for time at peak profile temperature (tp) is defined as supplier minimum and a user maximum.

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Request Samples (>)



ESD Sensitive (Pb)

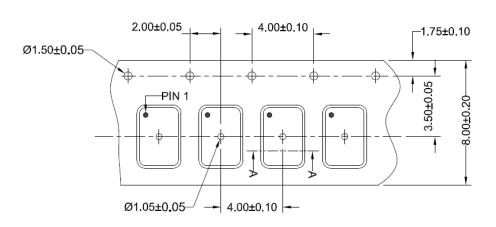
Check Inventory (>)

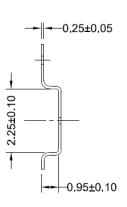


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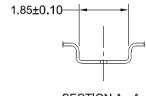
Packaging

T: 1,000pcs/reel T3: 3,000pcs/reel

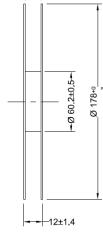




FEEDING (PULL) DIRECTION



SECTION A - A



----9.0±0.3

Dimensions: mm

Ø13.2±0.5

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