

#### AANI-CH-0056

Request Samples



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33 x 6 x 3 mm **RoHS/RoHS II Compliant** MSL Level = N/A

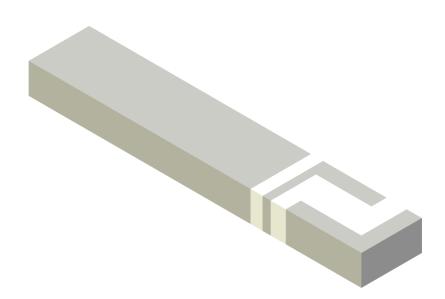
#### **Features**

- Ultra-wide band 790MHz ~ 2700MHz
- Compact & Low-Profile
- $VSWR: \le 2.0$
- Peak Gain: 4.5 dBi
- Efficiency: up to 70 %
- Surface Mount (SMD)
- High efficiency across cellular bands

### **Applications**

- Internet of Things (IoT) devices
- LTE-M, Cat M, Cat M1, NB-IoT
- LTE CAT 1 up to CAT 20 supported
- CBRS and 5G bands supported
- Smart Home networking
- Networking & Telecommunication

### **Product Image**







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### **Electrical Specification**

Parameter	Specification		Unit
Operating Frequency	790-960 1700-2700		MHz
VSWR (Typ.)	< 2.0		
Peak Gain	1.5 4.5		dBi
Efficiency (Maximum)	45	70	%
Impedance	50		Ω
Polarization	Linear		
Radiation Pattern (Azimuth)	Omni-di		

Note: All test measurements were conducted on 120 x 45 mm. Performance of the chip antenna will vary relative to the ground plane size in use.

### **Mechanical Specification**

Parameter	Specification
Dimensions	33 x 6 x 3 mm
Evaluation Board size	130 x 60 mm
Recommended Ground Clearance for Antenna	60 x 9.8 mm
Mounting Type	Surface Mount
Material(s)	Ceramic

### **Environmental Specification**

Parameter	Specification
Operating Temperature	-40°C to +85°C
Storage Temperature	-40°C to +85°C
Relative Humidity	90% to 95%
RoHS Complaint	Yes





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### **Ordering Information**

Part Number	Description
AANI-CH-0056	Embedded LTE Chip Antenna
AANI-CH-0056-EVB	Embedded LTE Chip Antenna Evaluation Board



**REVISED: 08-08-24** 



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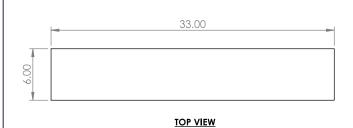


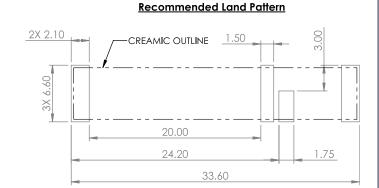
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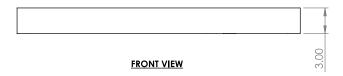


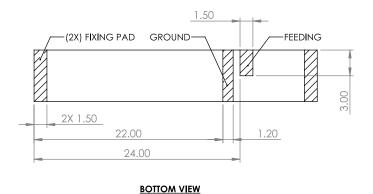
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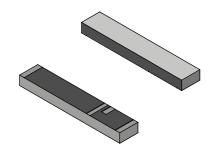
### **Product & Footprint & Land Pattern Dimensions**











Unit: mm





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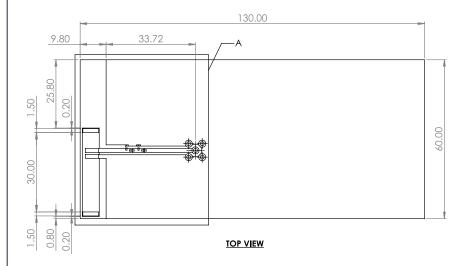


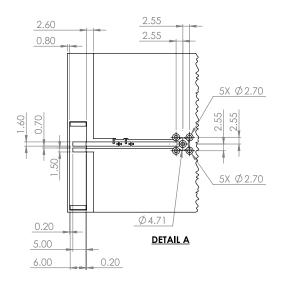
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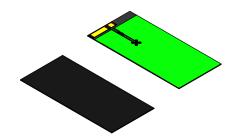
 $33 \times 6 \times 3 \text{ mm}$ RoHS/RoHS II Compliant MSL Level = N/A

### **Evaluation Board Dimensions**









Unit: mm





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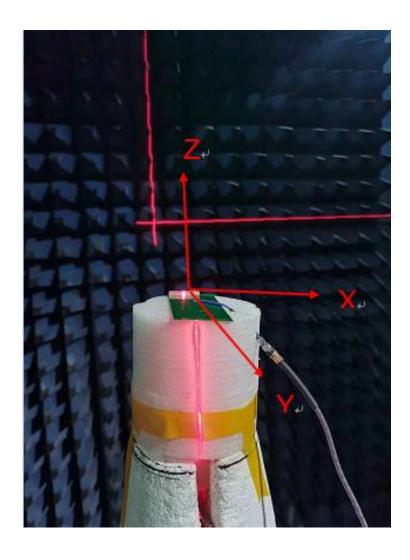


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### **Measurement Setup photo**







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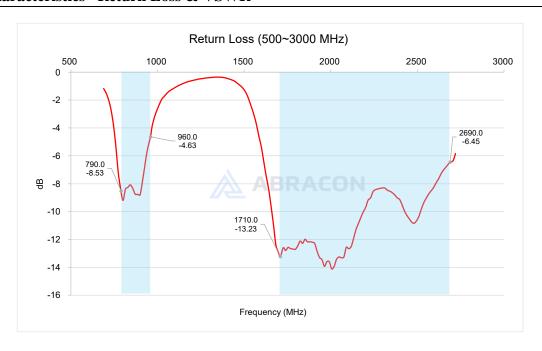


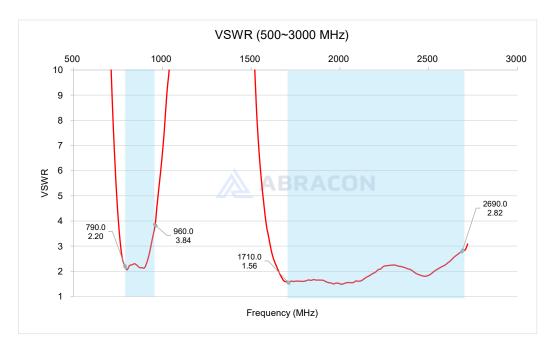
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#### Reflection Characteristics - Return Loss & VSWR









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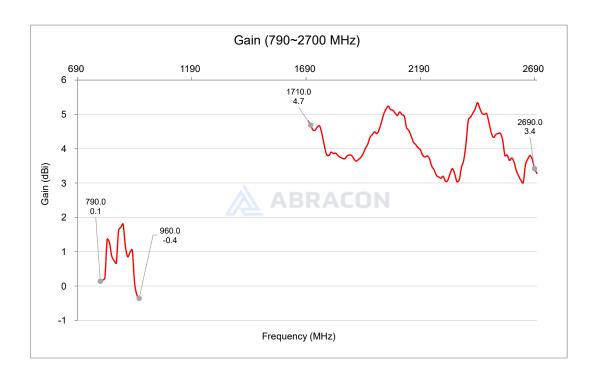


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### Radiation Characteristics - Peak Gain







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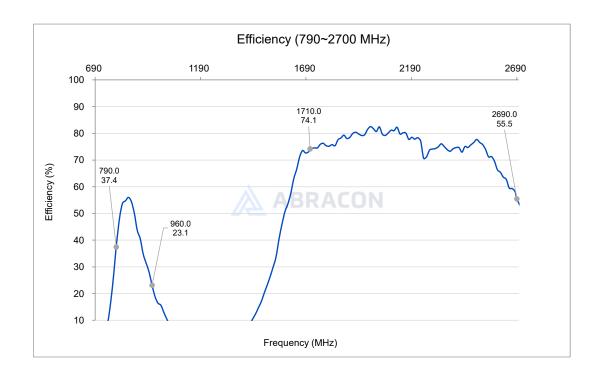


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### **Radiation Characteristics – Total Efficiency (%)**







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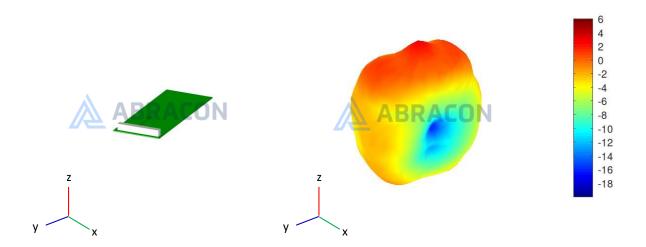


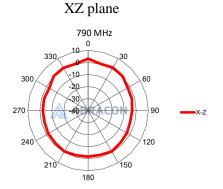
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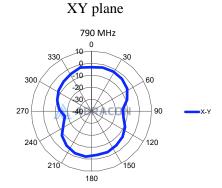


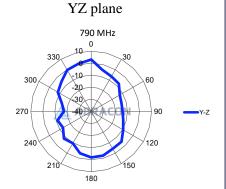
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### Radiation Characteristics – 3D & 2D Pattern













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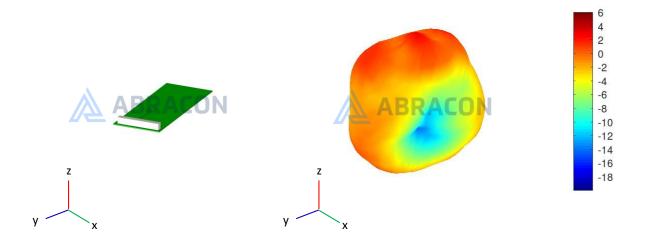
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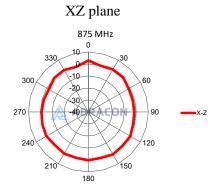


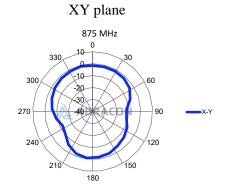
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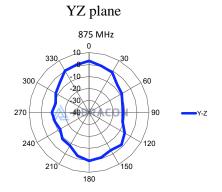


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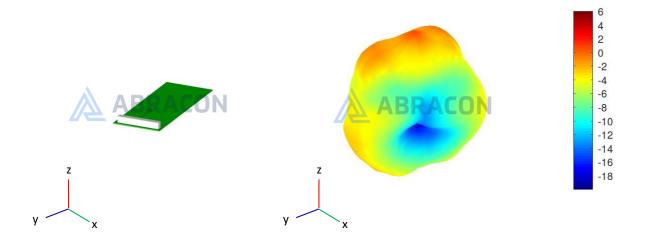
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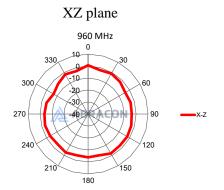


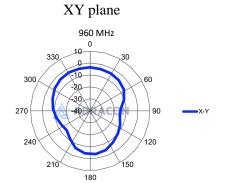
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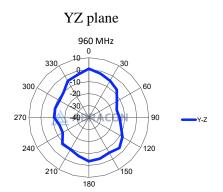


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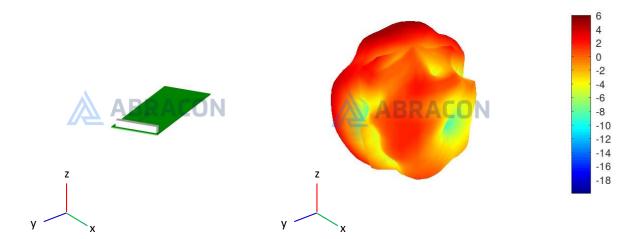
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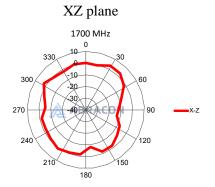


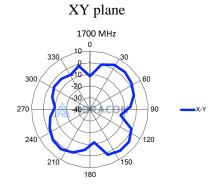
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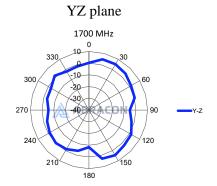


 $33 \times 6 \times 3 \text{ mm}$ RoHS/RoHS II Compliant MSL Level = N/A













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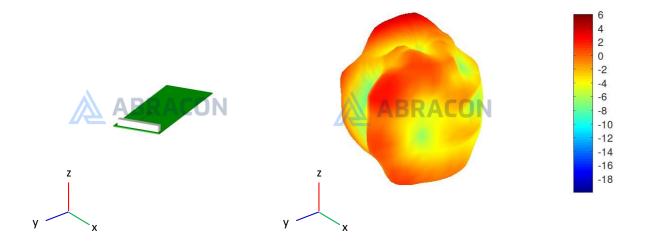
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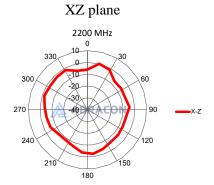


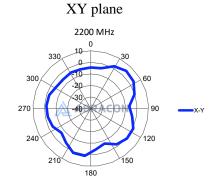
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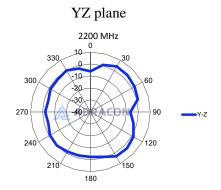


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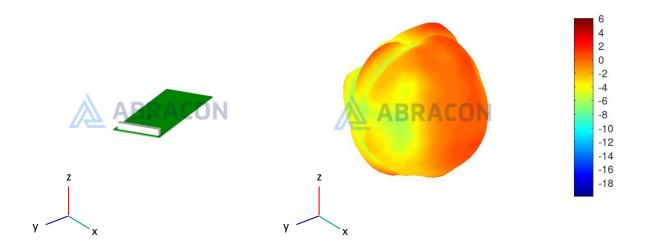
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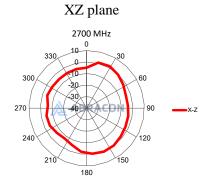


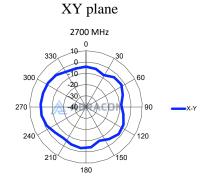
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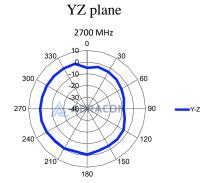


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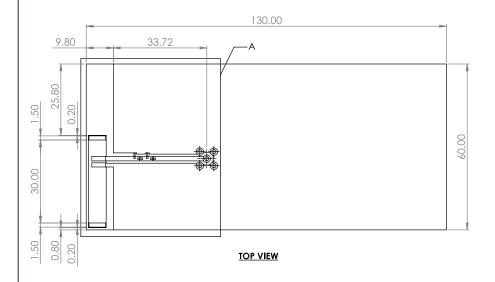
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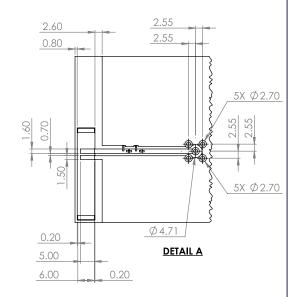


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### **Evaluation Board Outline & Matching Circuit**

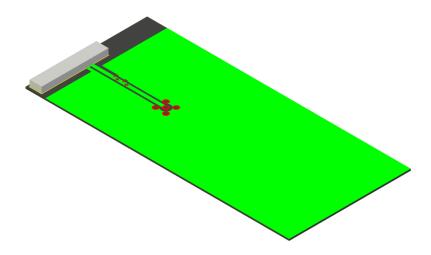
The evaluation board (<AANI-CH-0056-EVB >) is developed to simplify antenna testing and evaluation. It has an arbitrary size of 130 x 60 mm. The purpose is to give a reference design for an optimal antenna implementation. The evaluation board can also be used to test other implementations by cutting and soldering the PCB into any device.





Unit: mm

#### Evaluation board outline







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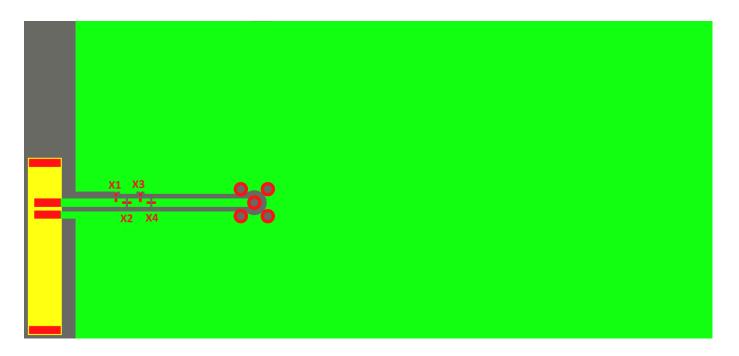


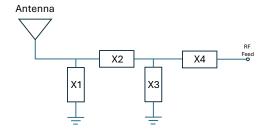
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The evaluation board has a matching circuit implemented next to the antenna. This is aimed to enable optimization possibilities for the user.

The antenna needs a matching circuit to adjust the resonant frequency balance. When delivered, the evaluation board is tuned for optimum balance at the LTE frequency bands using the following (can be replaced by equivalent):

However, it is common that the resonant frequency will shift during implementation in an arbitrary device. Therefore, this matching may be changed with other values/components/brands for compensation of such effects. This is further described in the General Implementation Guidelines section below.





Location	Description
X1	6.2 nH
X2	3.0 pF
X3	N/C
X4	0 ohm





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### **Reflow Profile [JEDEC J-STD-020]**

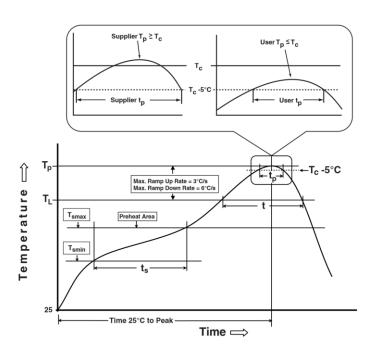


Table 1

SnPb Eutectic Process Classification Temperatures (T <sub>c</sub> )					
Package Volume mm³ Volume mm³					
Thickness	<350	≥350			
<2.5mm	235°C	220°C			
≧2.5mm	220°C	220°C			

#### Table 2

Pb-Free Process Classification Temperatures (T₀)						
Package Thickness	Volume mm³ <350	Volume mm³ 350-2000	Volume mm³ >2000			
<1.6mm	260°C	260°C	260°C			
1.6mm - 2.5mm	260°C	250°C	245°C			
>2.5mm	250°C	245°C	245°C			

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Preheat / soak		
Temperature minimum (T <sub>smin</sub> )	100°C	130°C
Temperature maximum (T <sub>smax</sub> )	150°C	175°C
Time (T <sub>smin</sub> to T <sub>smax</sub> ) (t <sub>s</sub> )	60 – 120 sec.	60 – 120 sec.
Average ramp-up rate (T <sub>smax</sub> to T <sub>p</sub> )	3°C/sec. max	3°C/sec. max
Liquidous temperature (T <sub>L</sub> )	183°C	217°C
Time at Liquidous (T <sub>L</sub> )	60 – 150 sec.	60 – 150 sec.
Peak package body temperature (T <sub>p</sub> )*	See Table 1	See Table 2
Time $(T_p)^{**}$ within 5°C of the specified classification temperature $(T_c)$	20 sec.	30 sec.
Ramp-down rate (T <sub>p</sub> to T <sub>smax</sub> )	6°C/sec. max	6°C/sec. max
Time 25°C to peak temperature	6 min. max	8 min. max
Reflow cycles	2 max	2 max

<sup>\*</sup>Tolerance for peak profile temperature  $(T_p)$  is defined as a supplier minimum and a user maximum.



<sup>\*\*</sup>Tolerance for time at peak profile temperature (tp) is defined as a supplier minimum and a user maximum.



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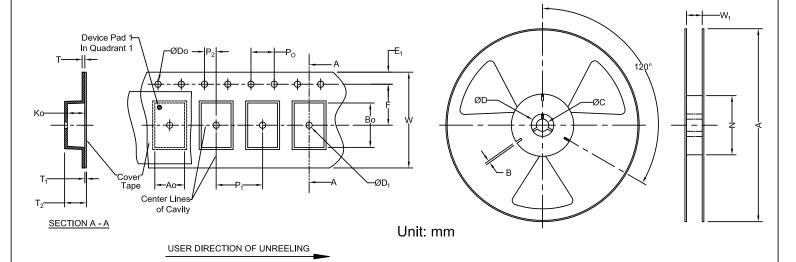
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### **Packaging**

#### Tape & Reel Dimension



	Carrier Tape Specifications (mm)								
Do	Ko	Во	E <sub>1</sub>	P <sub>0</sub>	$P_2$	F	P <sub>1</sub>	W	Reel Qty
1.50 ± 0.1	3.4 ± 0.1	38.4 ± 0.1	1.75 ± 0.1	4.0 ± 0.1	2.0 ± 0.1	26.2 ± 0.1	12.0 ± 0.1	56 ± 0.30	600

Reel Specifications (mm)				
A W <sub>1</sub>				
330 ± 1.5	57 ± 0.5			

ATTENTION: Abracon LLC's products are Commercial-Off-The-Shelf ('COTS'), which are designed, intended, and validated for use in commercial, industrial, and automotive applications. The customer is responsible for testing and verifying the performance of an Abracon solution to meet their system-level requirements.

