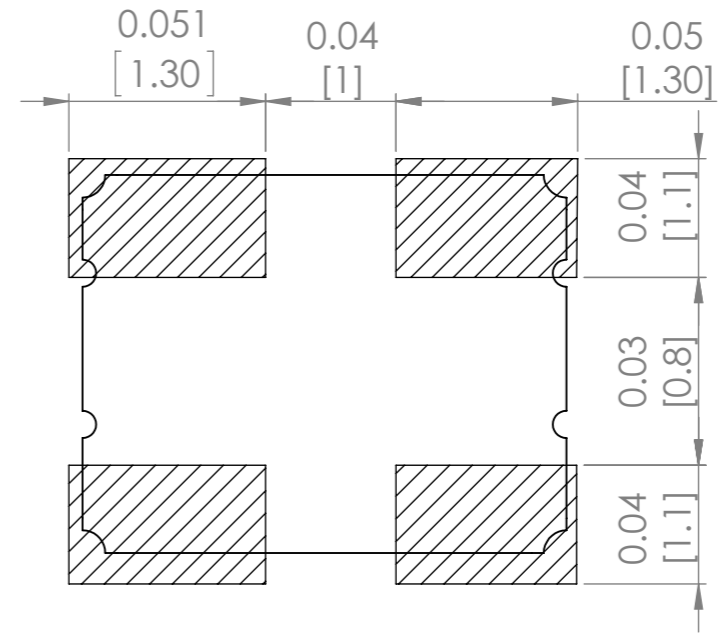
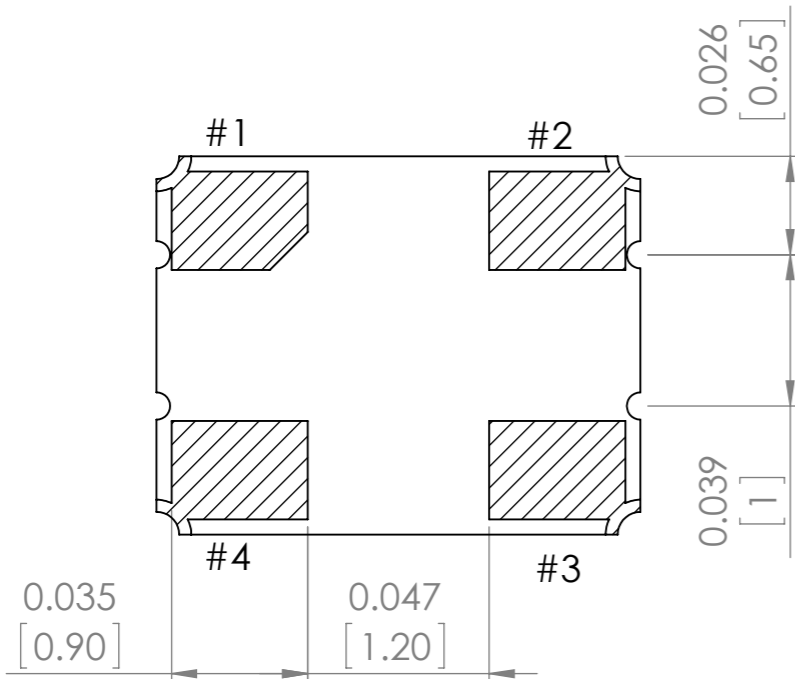
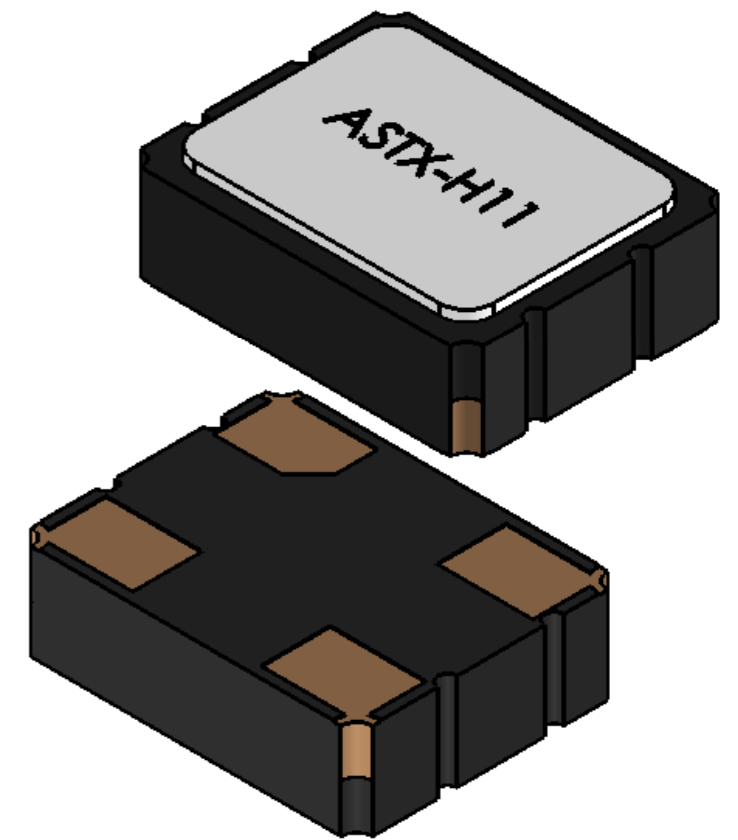


**Recommended Land Pattern**




Tri-state pin	Output
High or floating	Active
Low	Hi-impedance

Pin	Function
1	Tri-state
2	GND
3	Output
4	Vdd



Note: Recommend using an approximately 0.01µF bypass capacitor between Pin 2 and 4.

UNLESS OTHERWISE SPECIFIED: DIMENSIONS ARE IN INCH(MM) SURFACE FINISH: TOLERANCES: LINEAR: ANGULAR:				FINISH:	DEBUR AND BREAK SHARP EDGES	DO NOT SCALE DRAWING	REVISION -
DRAWN	-	SIGNATURE	DATE			 30332 Esperanza, Rancho Santa margarita, California 92688	
CHK'D	XXXXXX					TITLE: Oscillator	
APPV'D						DWG NO.	ASTX-H11
MFG					MATERIAL:		A3
Q.A					WEIGHT:	SCALE:20:1	SHEET 1 OF 1