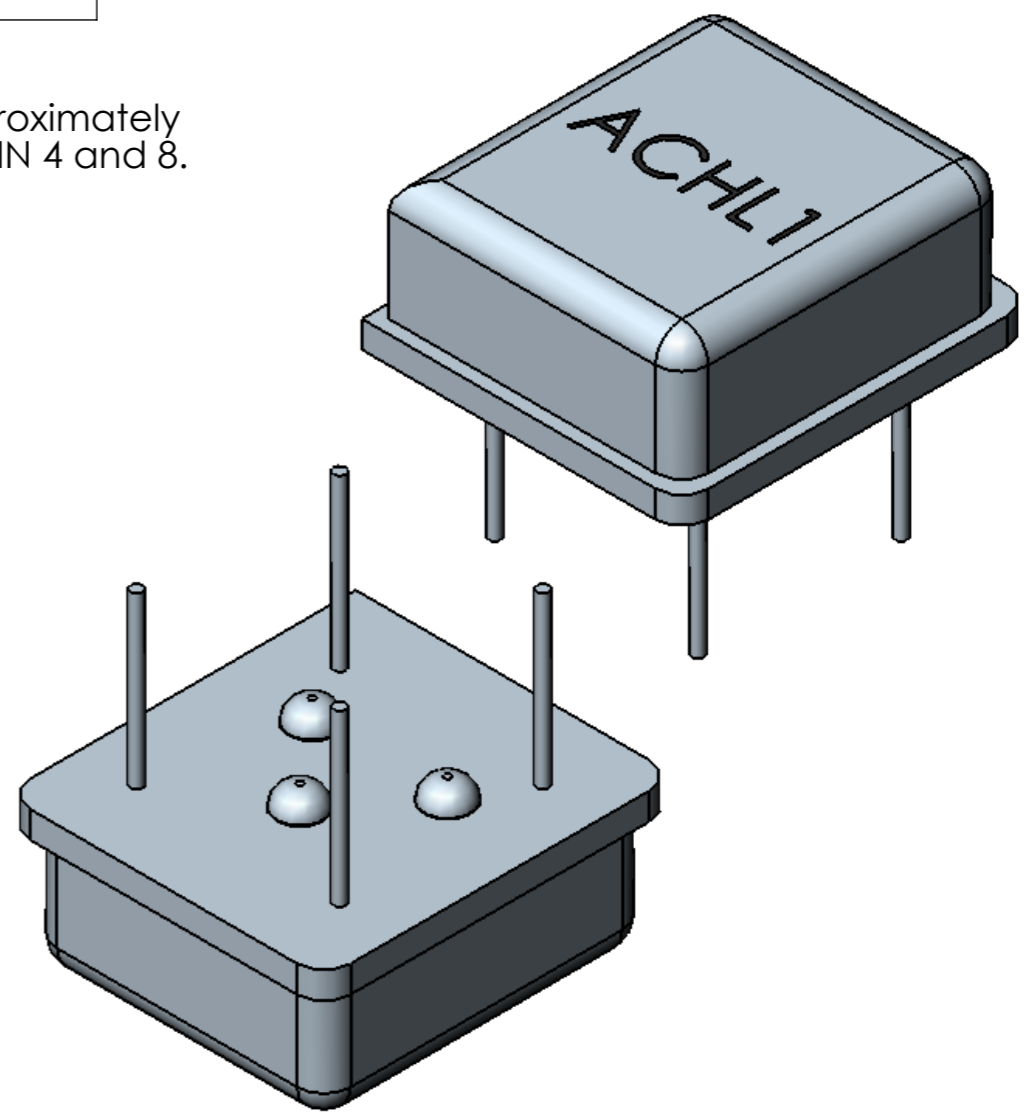



PIN	FUNCTION
1	NC or Tristate Enable/ Disable
4	GND/Case
5	Output
8	VDD

Note: Recommended using an approximately 0.01uF bypass capacitor between PIN 4 and 8.



UNLESS OTHERWISE SPECIFIED: DIMENSIONS ARE IN INCH(MM) SURFACE FINISH: TOLERANCES: LINEAR: ANGULAR:				FINISH:	DEBUR AND BREAK SHARP EDGES	DO NOT SCALE DRAWING	REVISION
DRAWN SAAZVAT				SIGNATURE	DATE	 30332 Esperanza, Rancho Santa margarita, California 92688	
CHK'D XXXXXX						TITLE: OSCILLATOR	
APPV'D						DWG NO. ACHL1	
MFG				MATERIAL:		A3	
Q.A				WEIGHT:		SCALE:5:1 SHEET 1 OF 1	