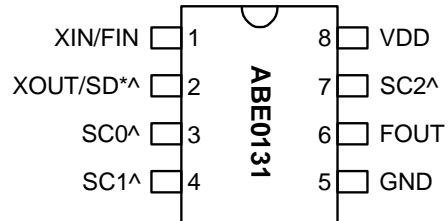


Low EMI Spread Spectrum Multiplier Clock

FEATURE

- Spread Spectrum Clock Generator with 1x outputs.
- Output frequency ranges: 10MHz to 30MHz.
- Accepts input from crystal or reference clock.
- Selectable Center, Down or Asymmetric Spread Modulation.
- Selectable Modulation magnitude.
- TTL/CMOS compatible outputs.
- 3.0V (+/-10%) Supply Voltage.
- Low short-term jitter.
- Available in 8-Pin 150mil SOIC.

PIN CONFIGURATION



XIN/FIN = 10 ~ 30 MHz

Note: ^: Internal pull-up resistor (120kΩ for SD, 30 kΩ for SC0-SC2).

*: The value of SD is latched upon power-up. The internal pull-up resistor results in a default high value when no pull-down resistor is connected to this pin (recommended external pull-down resistor of 27 kΩ).

DESCRIPTION

The ABE0131 is a Spread Spectrum Clock Generator designed to reduce EMI in high-speed digital systems. The device is designed to operate from a crystal or reference clock input and provides a 1x modulated clock output. Center, Down and Asymmetric spread types are selectable as well as the modulation magnitude.

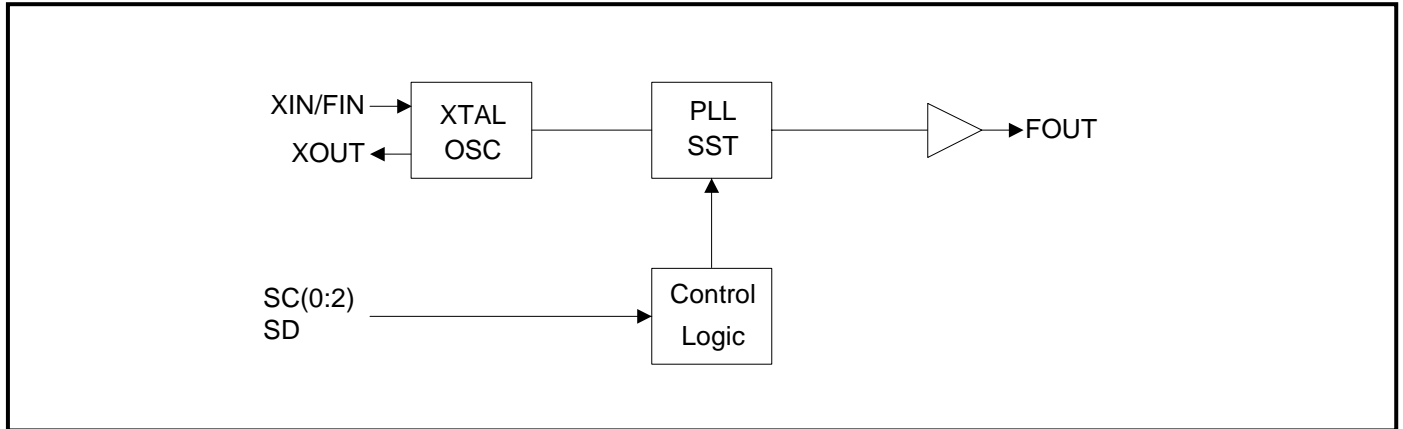
OUTPUT CLOCK SELECTION

SD	SC2	SC1	SC0	SST Modulation			
				Magnitude	Freq.	Type	
0	1	1	0	3.75%	Fin / 512	D	-3.75%
1	0	0	1	3.50%		D	-3.50%
1	0	1	0	3.75%		A	+0.125%, -3.625%
1	1	0	0	3.25%		D	-3.25%
1	1	0	1	3.50%		A	+0.125%, -3.375%
1	1	1	0	3.75%		A	+0.25%, -3.5%
0	0	0	0	3.25%		C	+/-1.625 %
0	0	0	1	3.50%		C	+/-1.75%
0	0	1	0	3.75%		C	+/-1.875%
0	0	1	1	0.00%			SST OFF

Notes: A: Asymmetric Spread. D: Down Spread. C: Center Spread.

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BLOCK DIAGRAM



PIN DESCRIPTIONS

Name	Number	Type	Description
XIN/FIN	1	I	Crystal input to be connected to fundamental parallel mode crystal. (C _L =18pF) or clock input.
XOUT/SD	2	B	At power-up, this pin is an input pin to select modulation magnitude and type. After input sampling, this pin is crystal output. Has internal pull up resistor.
SC0	3	I	Digital control input to select modulation magnitude and type. Has internal pull-up.
SC1	4	I	Digital control input to select modulation magnitude and type. Has internal pull-up.
SC2	7	I	Digital control input to select modulation magnitude and type. Has internal pull-up.
VDD	8	P	3.0(+/-10%)V Power Supply.
FOUT	6	O	Modulated Clock Frequency Output.
GND	5	P	Ground.

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FUNCTIONAL DESCRIPTION

Selectable spread spectrum modulation types and magnitudes

The ABE0131 provides selectable spread spectrum modulation type, as well as selectable modulation magnitude. Selection is made by connecting specific pins to a logical "zero" or "one", according to the output clock selection table on page 1.

In order to reduce the number of pins on the chip, the ABE0131 uses pin 2 (XOUT/SD) as a bi-directional pin. The pin serves as a modulation type and magnitude selector input (SD) upon power-up (see output clock selection table on page 1), and as XOUT crystal connection as soon as the input has been latched.

Pins 3 (SC0), 4 (SC1), and 7 (SC2) are used as inputs to complete the spread spectrum modulation type and magnitude selection as shown on the output clock selection table (page 1).

Connecting a selection pin to a logical "one"

All selection pins have an internal pull-up resistor (30k Ω for pins 3, 4, 7, and 120k Ω for pin 2). This internal pull-up resistor will pull the input value to a logical "one" by default, i.e. when no connection is made between the pin and GND. No external pull-up resistor is therefore required for connecting a logical "one" upon power-up.

Connecting a selection pin to a logical "zero"

For an input only pin, i.e. pins 3 (SC0), 4 (SC1), and 7 (SC2), the pin simply needs to be grounded to pull the input down to a logical "zero". Pin 2 (XOUT/SD) should be connected to GND thru a 27k Ω resistor to select a logical "zero".

ELECTRICAL SPECIFICATIONS

1. Absolute Maximum Ratings

PARAMETERS	SYMBOL	MIN.	MAX.	UNITS
Supply Voltage	V _{DD}		4.6	V
Input Voltage, dc	V _i	-0.5	V _{DD} +0.5	V
Output Voltage, dc	V _o	-0.5	V _{DD} +0.5	V
Storage Temperature	T _S	-65	150	°C
Ambient Operating Temperature*	T _A	-40	85	°C
Junction Temperature	T _J		125	°C
Lead Temperature (soldering, 10s)			260	°C
ESD Protection, Human Body Model			2	kV

Exposure of the device under conditions beyond the limits specified by Maximum Ratings for extended periods may cause permanent damage to the device and affect product reliability. These conditions represent a stress rating only, and functional operations of the device at these or any other conditions above the operational limits noted in this specification is not implied.

* Note: Operating Temperature is guaranteed by design for all parts (COMMERCIAL and INDUSTRIAL), but tested for COMMERCIAL grade only.

Low EMI Spread Spectrum Multiplier Clock

2. DC/AC Specifications

PARAMETERS	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNITS
Supply Voltage	V _{DD}		2.7	3.0	3.3	V
Input High Voltage	V _{IH}		0.7* V _{DD}			V
Input Low Voltage	V _{IL}				0.3* V _{DD}	V
Input High Current	I _{IH}				100	μA
Input Low Current	I _{IL}				100	μA
Output High Voltage	V _{OH}	I _{OH} =5mA, V _{DD} =3.3V	2.4			
Output Low Voltage	V _{OL}	I _{OL} =6mA, V _{DD} =3.3V			0.4	
Input Frequency	F _{XIN}	When using a crystal	10		30	MHz
	F _{IN}	When using reference clock	10		30	MHz
Maximum interruption of F _{IN}		When using reference clock			100	μs
Load Capacitance	C _L	Between Pin XIN and XOUT*		18		pF
Pull-up Resistor	R _{up}	PIN 2		120		kΩ
Pull-up Resistor	R _{up}	PIN 3, 4, 7		30		kΩ
Short Circuit Current	I _{sc}			50		mA
3.3V Dynamic Supply Current	I _{CC}	No Load		18		mA

*Note: Pin XIN and XOUT each has a 36pF capacitance. When used with a XTAL, the two capacitors combined load the crystal with 18pF. If driving XIN with a reference clock signal, the load capacitance will be 36pF (typical).

3. Timing Characteristics

PARAMETERS	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNITS
Rise Time	T _r	Measured at 0.8V ~ 2.0V @ 3.3V	0.8	0.95	1.1	ns
Fall Time	T _f	Measured at 2.0V ~ 0.8V @ 3.3V	0.78	0.85	0.9	ns
Output Duty Cycle	D _T		45	50	55	%
Cycle to Cycle Jitter	T _{cyc-cyc}	FOUT=48MHz @ 3.3V			100	ps
Cycle to Cycle Jitter	T _{cyc-cyc}	FOUT=72MHz @ 3.3V			100	ps

