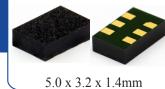
ASFLMX-25.000MHz-5ABH







**Moisture Sensitivity Level – MSL 3** 

### **FEATURES:**

- 25MHz LVCMOS
- Typical phase noise: 100fs (Integration range: 1.875MHz-20MHz)
- ±50ppm total frequency stability over -40°C to +85°C temperature range
- Industry standard 6-Pin 5 x 3.2mm LGA package

### **APPLICATIONS:**

- Communications
- Backplane reference clock
- SERDES reference clock
- FPGA

#### > KEY ELECTRICAL SPECIFICATIONS

#### **Absolute Maximum Ratings**

Item	Minimum	Maximum	Unit	Condition
Supply Voltage	-0.3	+3.6	V	
Storage Temp.	-55	+125	°C	
Lead Temp.(soldering, 10s)		+260	°C	
ESD (HBM)		2	kV	

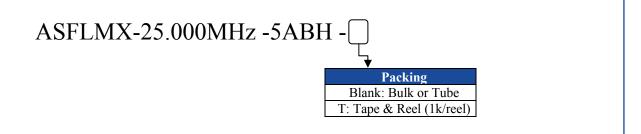
VDD = 2.375 - 3.63V, TA = -40°C to +85°C, outputs terminated with 50 Ohms to VDD /2.

	Parameters		Minimum	Typical	Maximum	Units	Notes
Frequency				25.000		MHz	
Operating Ter	mperature (T <sub>A</sub> )		-40		+85	°C	
Overall Frequ	ency Stability (2)		-50		+50	ppm	
Supply Voltag			+2.375		+3.63	V	
Supply Curre	nt (I <sub>DD</sub> )				95	mA	
Output Logic Level		$V_{OH}$	$V_{\rm DD}-0.8$			V	
		$V_{OL}$			0.6	V	
Input Logic Level $V_{IL}$ $V_{IL}$		2		$V_{DD} + 0.3$	V		
		$ m V_{IL}$	-0.3		0.8	V	
Start-up Time	<b>)</b>				20	ms	
Rise Time (Tr)		100		500	10.0	No load	
Fall Time (Tf)		100		500	ps	20% to 80%	
Duty Cycle			45		55	%	
Phase Noise	Integration Range: 12kHz to 20MHz			220		fsRMS	
	Integration Range: 1.875MHz to 20MHz			100			

#### **Notes:**

- 1. Guaranteed after thermal equilibrium
- 2. Inclusive of initial accuracy, temperature drift, aging, shock, vibration from -40°C to +85°C.

#### PART IDENTIFICATION

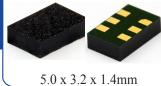




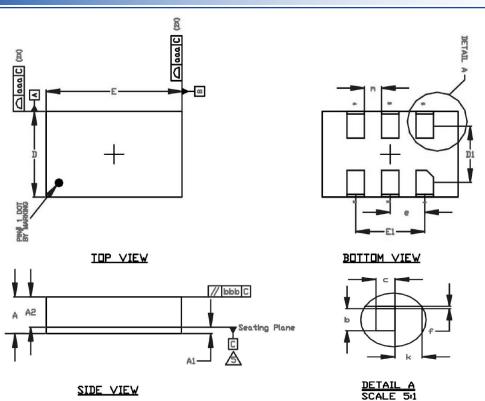


ASFLMX-25.000MHz-5ABH





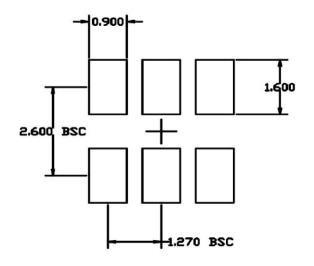
### **OUTLINE DIMENSION**



Ref.	Min.	Nom.	Max.
A	1.260	1.330	1.400
<b>A1</b>	0.190	0.230	0.270
<b>A2</b>	1.070	1.100	1.130
D	3.100	3.200	3.300
D1		2.100 BSC	7)
E	4.900	5.000	5.100
<b>E1</b>	2.540 BSC		
b	0.850	0.900	0.950
c	0.850	0.900	0.950
e		1.270 BSC	7
f	0.050	0.100	0.150
k	0.860	0.910	0.960
m	0.580	0.630	0.680
n		6	

Dimensional Tolerance		
aaa	0.100	
bbb	0.070	

### **Recommended Land Pattern**

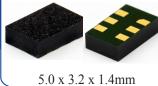


**Dimensions: mm** 

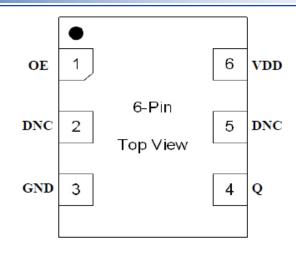


ASFLMX-25.000MHz-5ABH



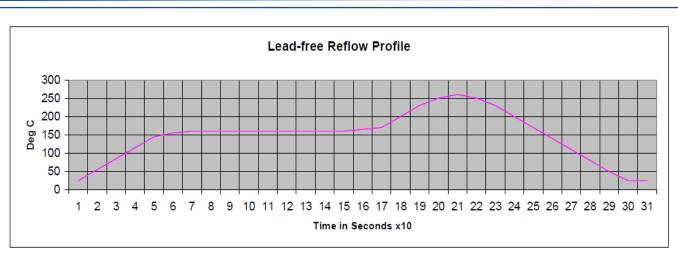


## PIN CONFIGURATION



Pin#	Pin Name	Pin Type	Pin Level	Pin Function
1	OE	I, SE	LVCMOS	Output Enable, disables output to tri-state.
1	OE	1, 512	LVCMOS	1 = Disabled, 0= Enabled, 50k Ω Pull-up
2	DNC			Make no connection, leave floating
3	GND	PWR		Power Supply Ground
4	Q	О	LVCMOS	Clock Output
5	DNC			Make no connection, leave floating
6	VDD	PWR		Power Supply

### **REFLOW PROFILE**



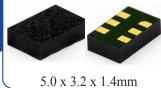
Parameters	Specifications
Average Ramp-up Rate	3°C /second max.
Pre-Heat Temp 150 – 200°C	60 – 180 second
Temp > 217°C	60 – 150 second
Time @ Peak Temperature	20 – 40 second
Peak Temperature	260°C + 0°C / -5°C
Ramp-down Rate	-6°C / second max.
Time 25°C to Peak Temp.	8 minutes max.

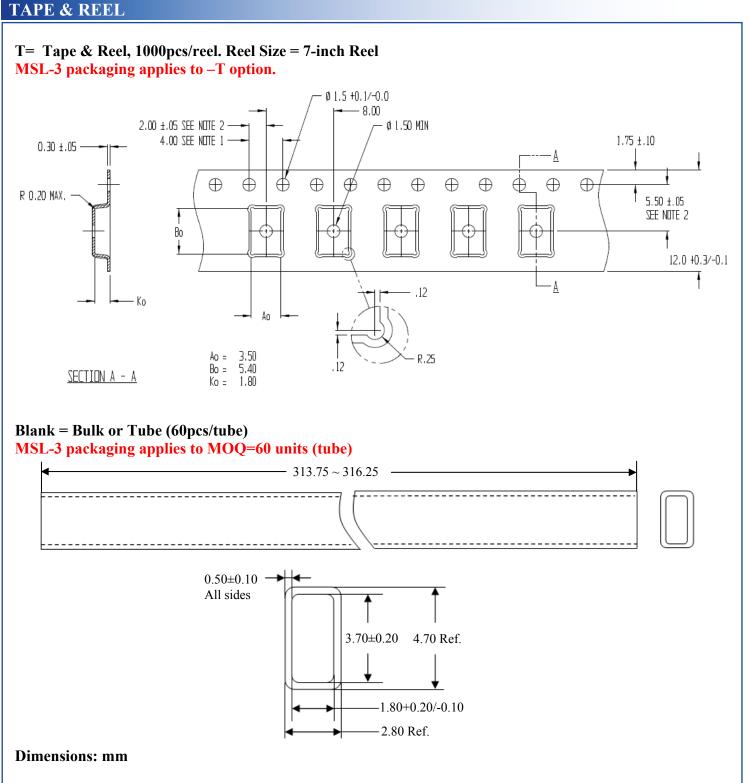




ASFLMX-25.000MHz-5ABH







ATTENTION: Abracon Corporation's products are COTS - Commercial-Off-The-Shelf products; suitable for Commercial, Industrial and, where designated, Automotive Applications. Abracon's products are not specifically designed for Military, Aviation, Aerospace, Life-dependant Medical applications or any application requiring high reliability where component failure could result in loss of life and/or property. For applications requiring high reliability and/or presenting an extreme operating environment, written consent and authorization from Abracon Corporation is required. Please contact Abracon Corporation for more information.



